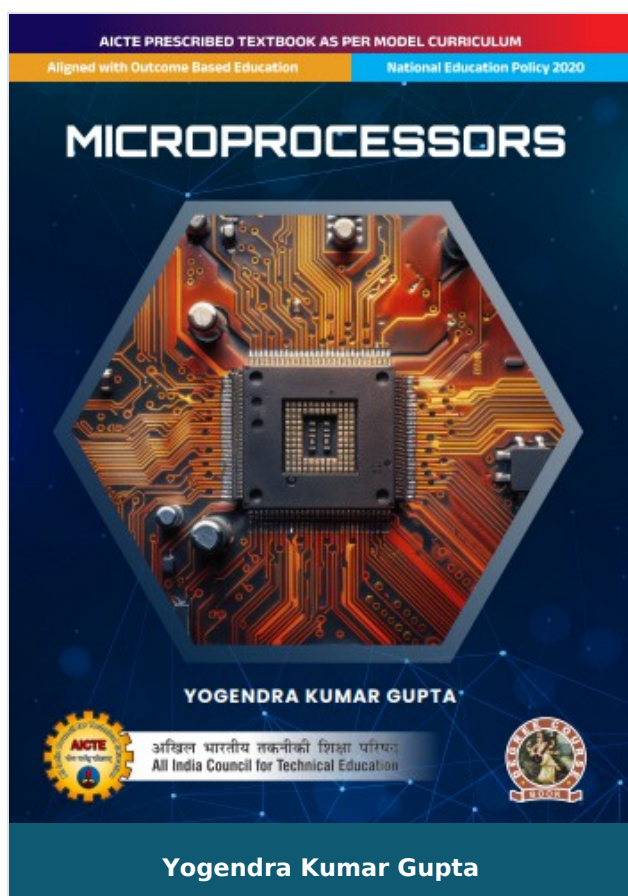




Microprocessors

Author : Yogendra Kumar Gupta

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Product Description

Microprocessors

This book contains topics of Microprocessors for the Vth semester students of Undergraduate degree in Electrical Engineering strictly as per syllabus and model curriculum of AICTE and aligned with the theme of outcome-based education according to National Education Policy 2020. This book comprises of five units covering topics on Fundamentals of Microprocessors, the 8051 Architecture, Instruction set and Programming, I/O Interfacing, Sensor Interfacing.

Salient features:

- Content of the book aligned with the mapping of Course Outcomes, Program Outcomes and Unit Outcomes.
- In the beginning of each unit learning outcomes are listed to make the student understand what is expected out of him/her after completing that unit.
- Book provides large number of examples, exercises, information beyond the unit coverage, QR code for E-Resources and references.
- Student and teacher centric subject materials included in book with balanced and chronological matter.
- Figures and tables inserted to improve clarity of topics.
- A 'Know More' section in each unit extends the learning beyond the syllabus.
- MCQs, short-answer type, long-answer type, numerical and practical questions added for the units to help in self-checking of knowledge.
- Answers have been provided for the MCQs and numerical questions. Hints have been given for solving the practical problems.

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Author

Yogendra Kumar Gupta

Dr. Yogendra Kumar Gupta Associate Professor, Dept. of Computer Science & Engineering, Swami Keshvanand Institute of Technology Management & Gramothan